

<b>Notice of References Cited</b>	Application/Control No. 10/563,646		Applicant(s)/Patent Under Reexamination PESSOLANO, FRANCESCO	
	Examiner Jacob Petranek		Art Unit 2183	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-7,143,272	11-2006	Wilkerson et al.	712/240
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chang, Po-Yung, Evers, Marius, Patt, Yale N. "Improving Branch Prediction Accuracy by Reducing Pattern History Table Interference" Proceedings of PACT '96. IEEE. Pages 48-57
	V	Simcha Gochman et al. "The Intel Pentium M Processor: Microarchitecture and Performance" Intel Technology Journal, Volume 7, issue 2, published May 21, 2003. Pages 21-36.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.